

FIG. 3A

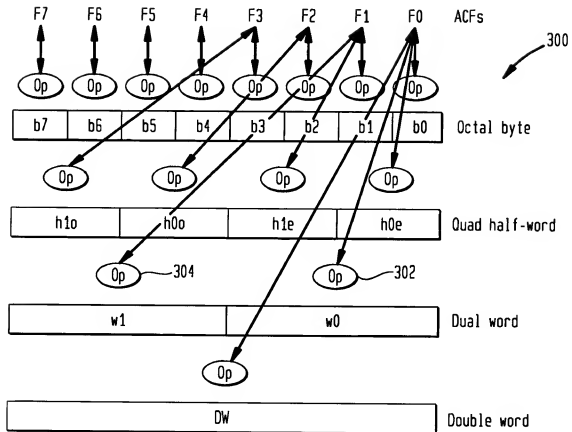


FIG. 3B

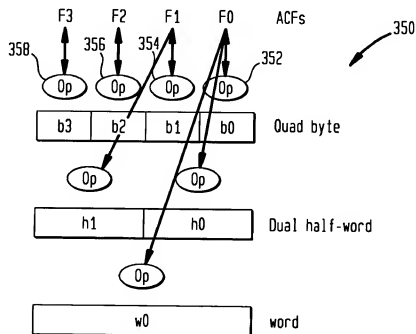


FIG. 4A

Load Direct Instruction Encoding																															
31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Group		S/P		Size		0		Amode		CE1		Rt		5-0		L/S		UADDR15													

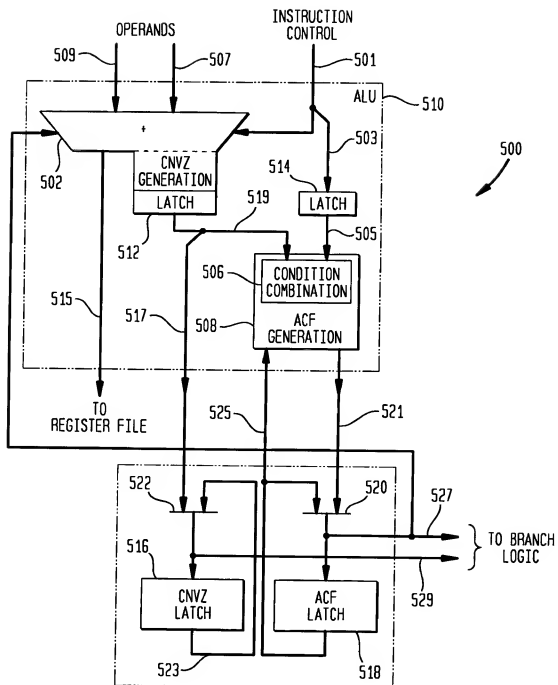
FIG. 4B

Copy Instruction Encoding																																				
31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0					
Group		S/P		Size		DSUpcode		Copy		Rt		4-0		Rx		4-0		Rt		5		Rx		5		0		CopyExt		CE2						

FIG. 4C

Logic Instruction Encoding																																	
31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0		
Group		S/P		Unit		ALUpcode		Logic Operation		Rt		Rte		0		Rx		Rx		Ry		Ry		0		Rxe		0		CE3b		LogicExt	

FIG. 5A



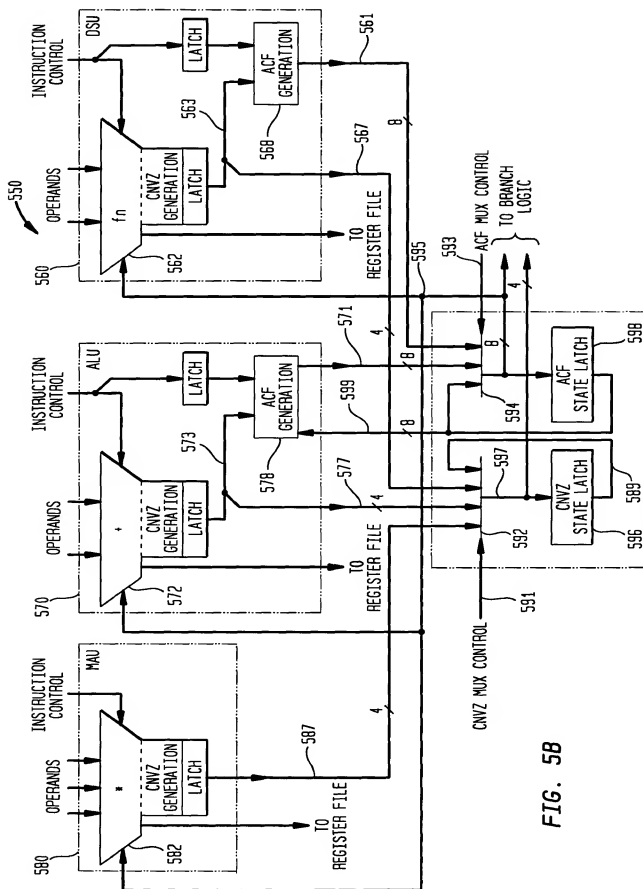


FIG. 5B